Docket No. 50269-0806

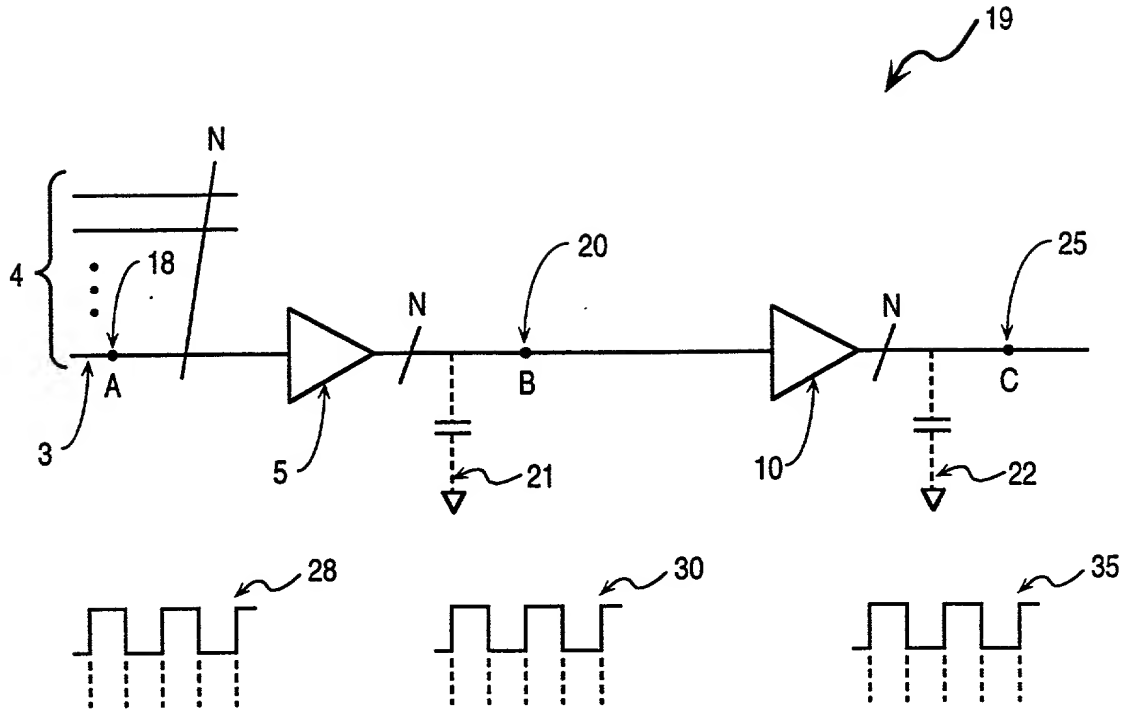


FIG. 1
(PRIOR ART)

TO:080" 958:2660

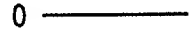
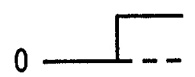
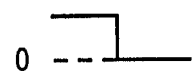
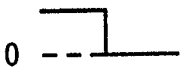
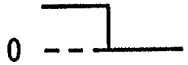
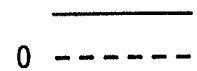
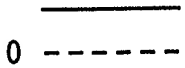
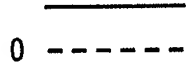
40 WAVEFORM AT "A"	42 NUMBER OF TOGGLES AT "A"	46 WAVEFORM AT "B"	48 NUMBER OF TOGGLES AT "B"	50 WAVEFORM AT "C"	52 NUMBER OF TOGGLES AT "C"	54 SUMMATION OF TOGGLES AT B & C Σ
	0		0		0	0
	1		1		1	2
	1		1		1	2
	0		0		0	0

FIG. 2
(PRIOR ART)

TO2080 "9583626O

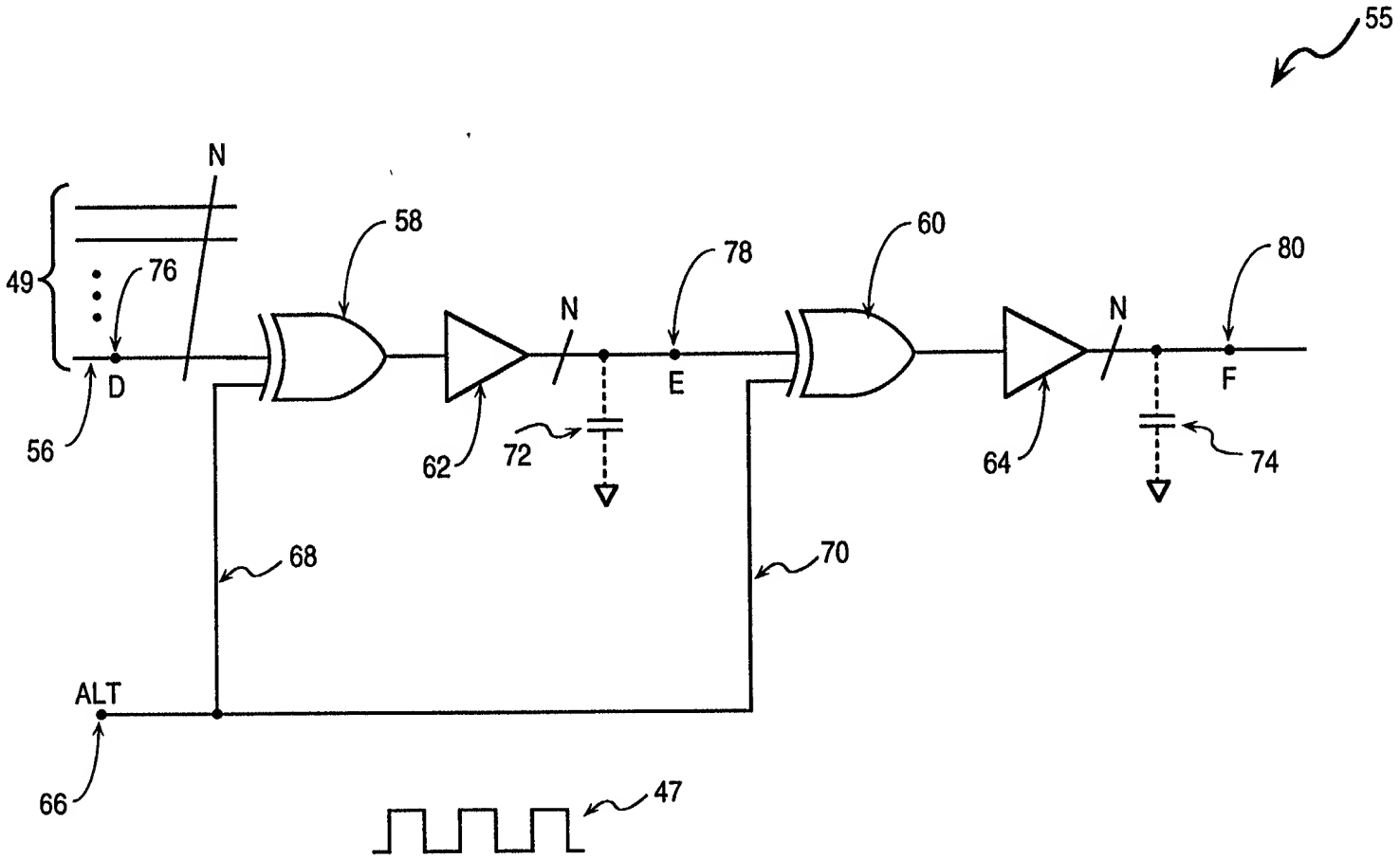


FIG. 3

WAVEFORMS AT "D" (41)	NUMBER OF TOGGLES AT "D" (43)	WAVEFORMS AT "ALT" (45)	WAVEFORMS AT "E" (47)	NUMBER OF TOGGLES AT "E" (49)	WAVEFORMS AT "F" (51)	NUMBER OF TOGGLES AT "F" (53)	SUMMATION OF TOGGLES AT "E" & "F" Σ (73)
82							
0	0	0	0	1	0	0	1
0	1	0	0	0	0	1	1
0	1	0	0	0	0	1	1
0	0	0	0	1	0	0	1
84							
0	0	0	0	1	0	0	1
0	1	0	0	0	0	1	1
0	1	0	0	0	0	1	1
0	0	0	0	1	0	0	1

FIG. 4

TO2080 "958H2660

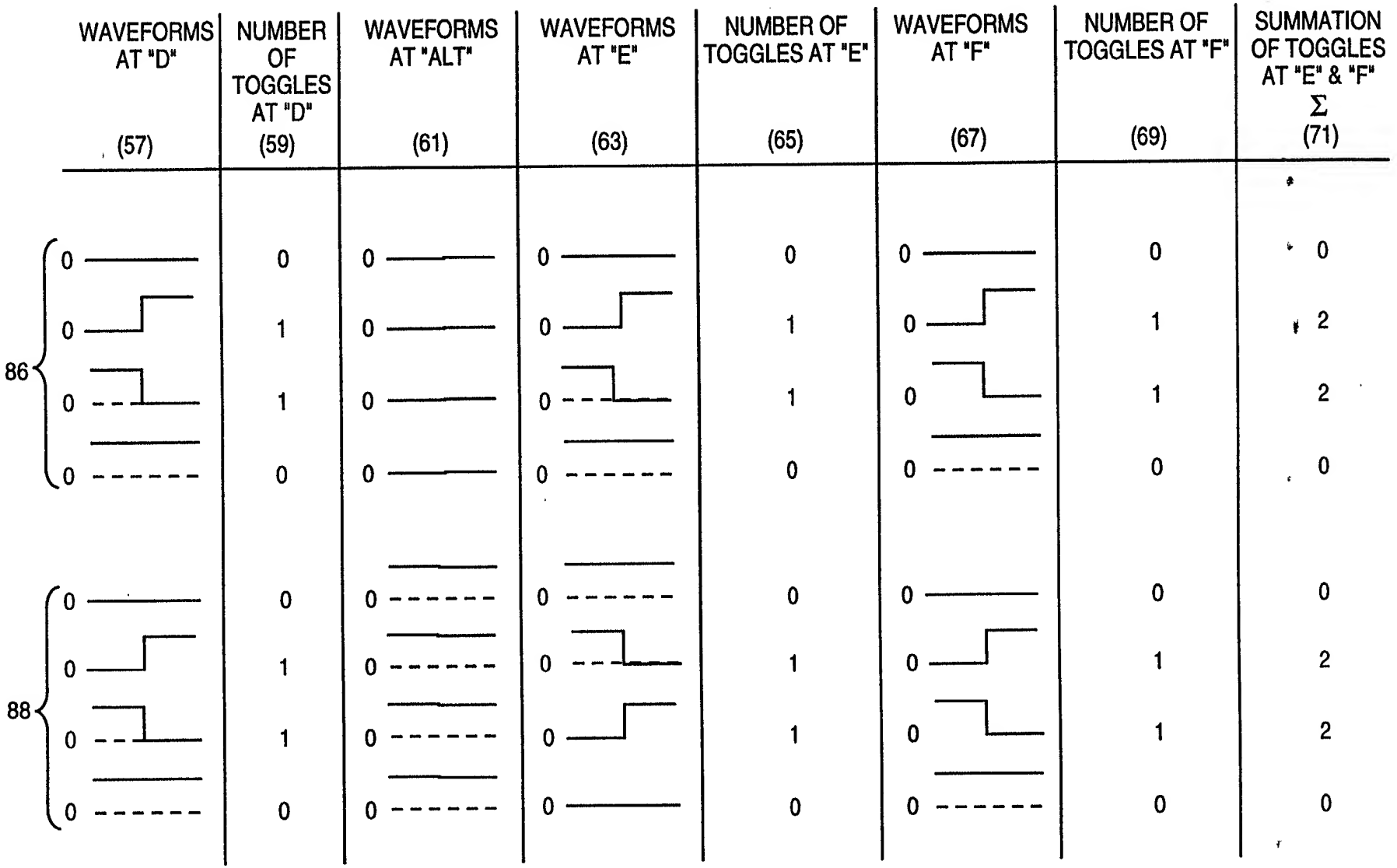


FIG. 5

PEAK SYSTEM POWER (APPROXIMATE)

	(90) WITHOUT ALT SIGNAL	(92) WITH ALT SIGNAL
• BEST CASE DATA PATTERN (91)	Ø WATTS	X/2 WATTS + Δ
• TYPICAL DATA PATTERN (93)	X/2 WATTS	X/2 WATTS + Δ
• WORST CASE DATA PATTERN (95)	X WATTS	X/2 WATTS + Δ

FIG. 6

Document: 0054850 - 08/05/04

TO2080 "958h2660

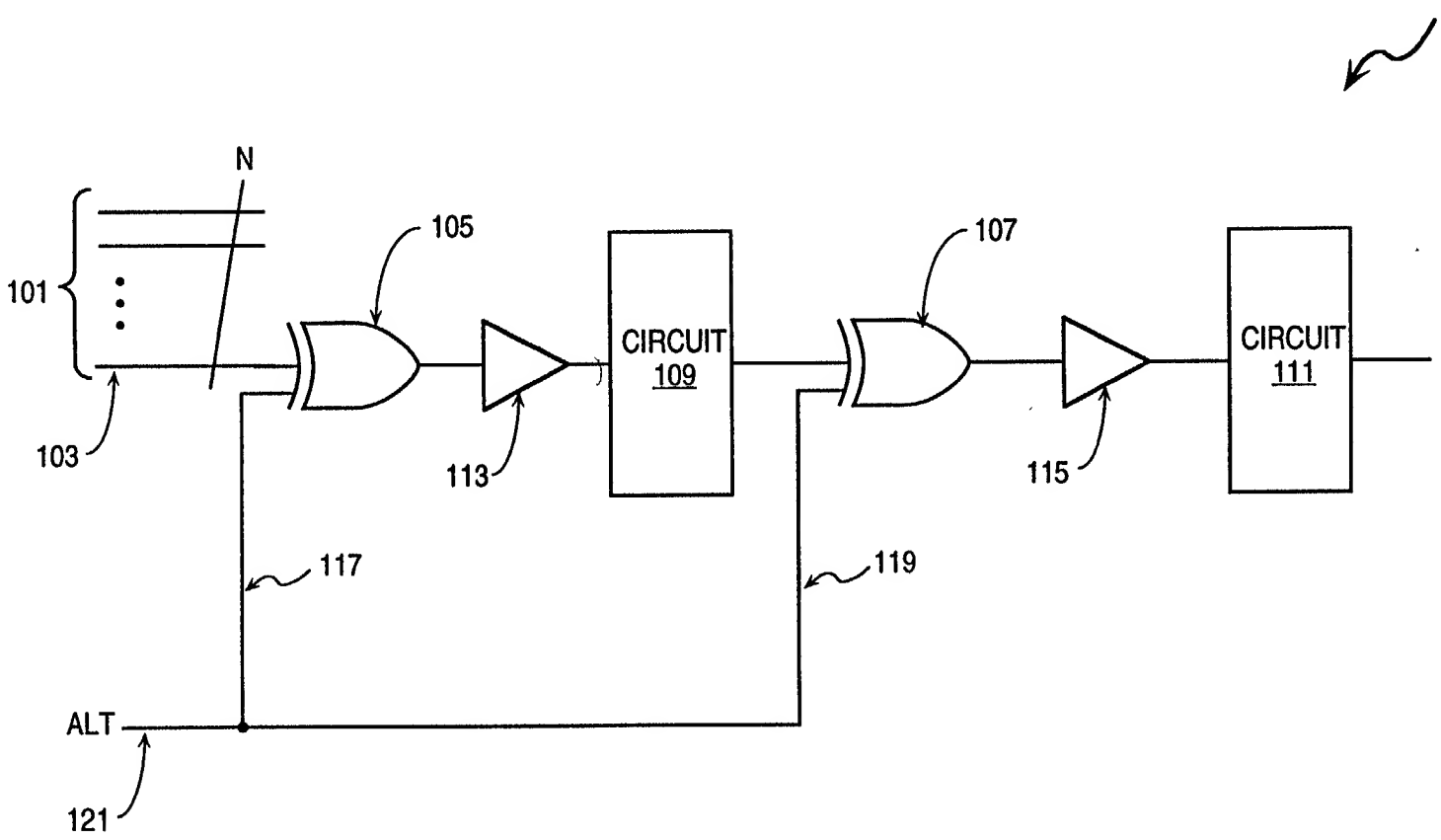


FIG. 7

TD2080 "95814660

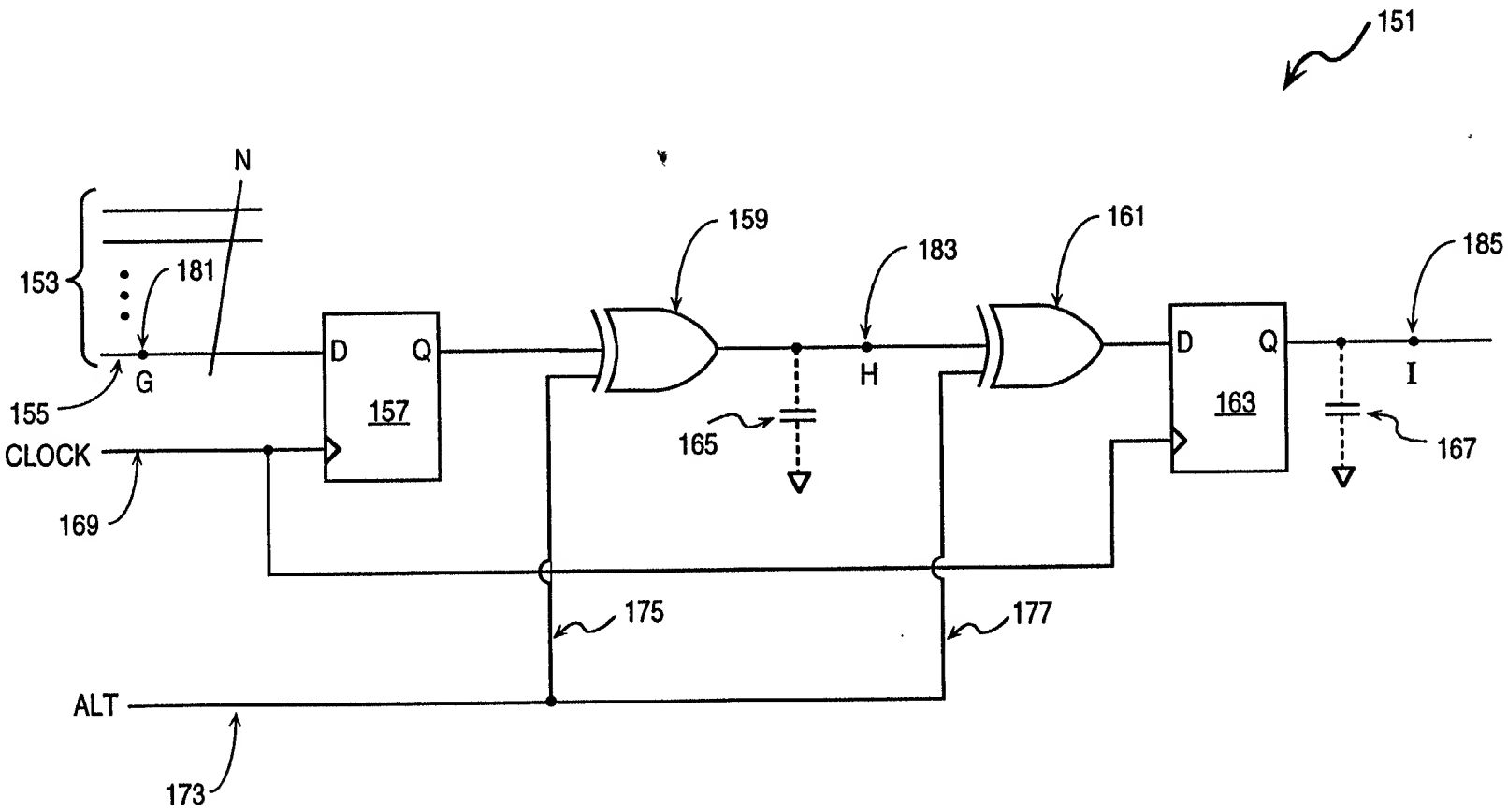


FIG. 8

TO2080 9581h2660

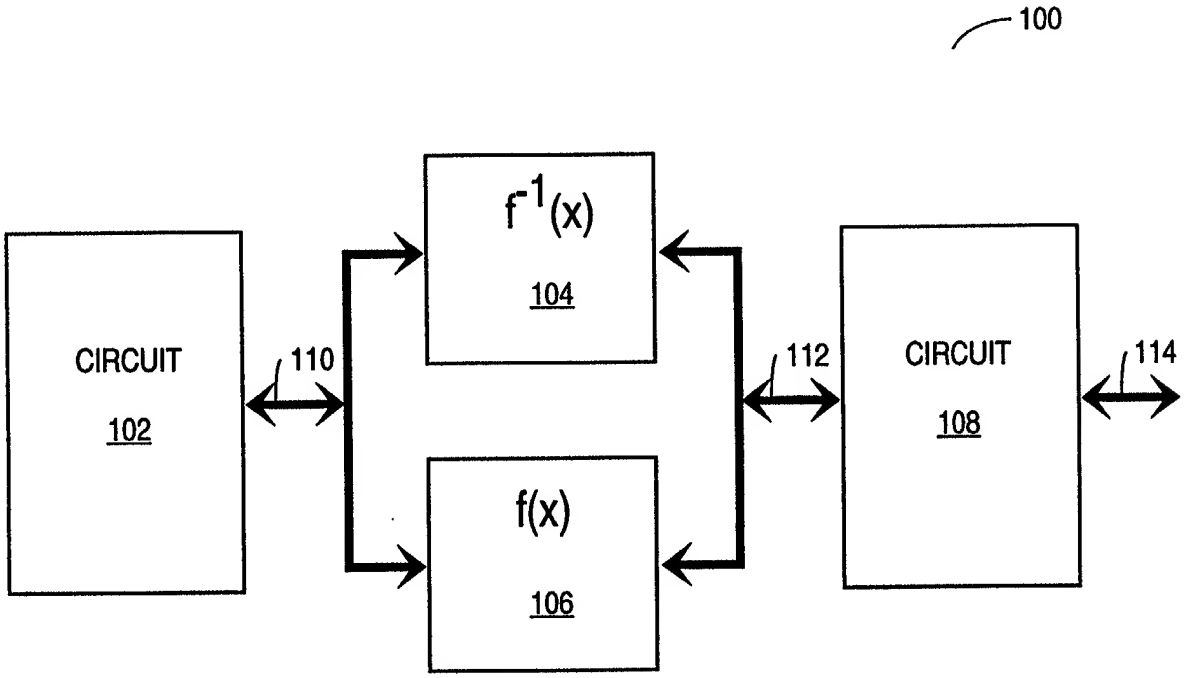


FIG. 9

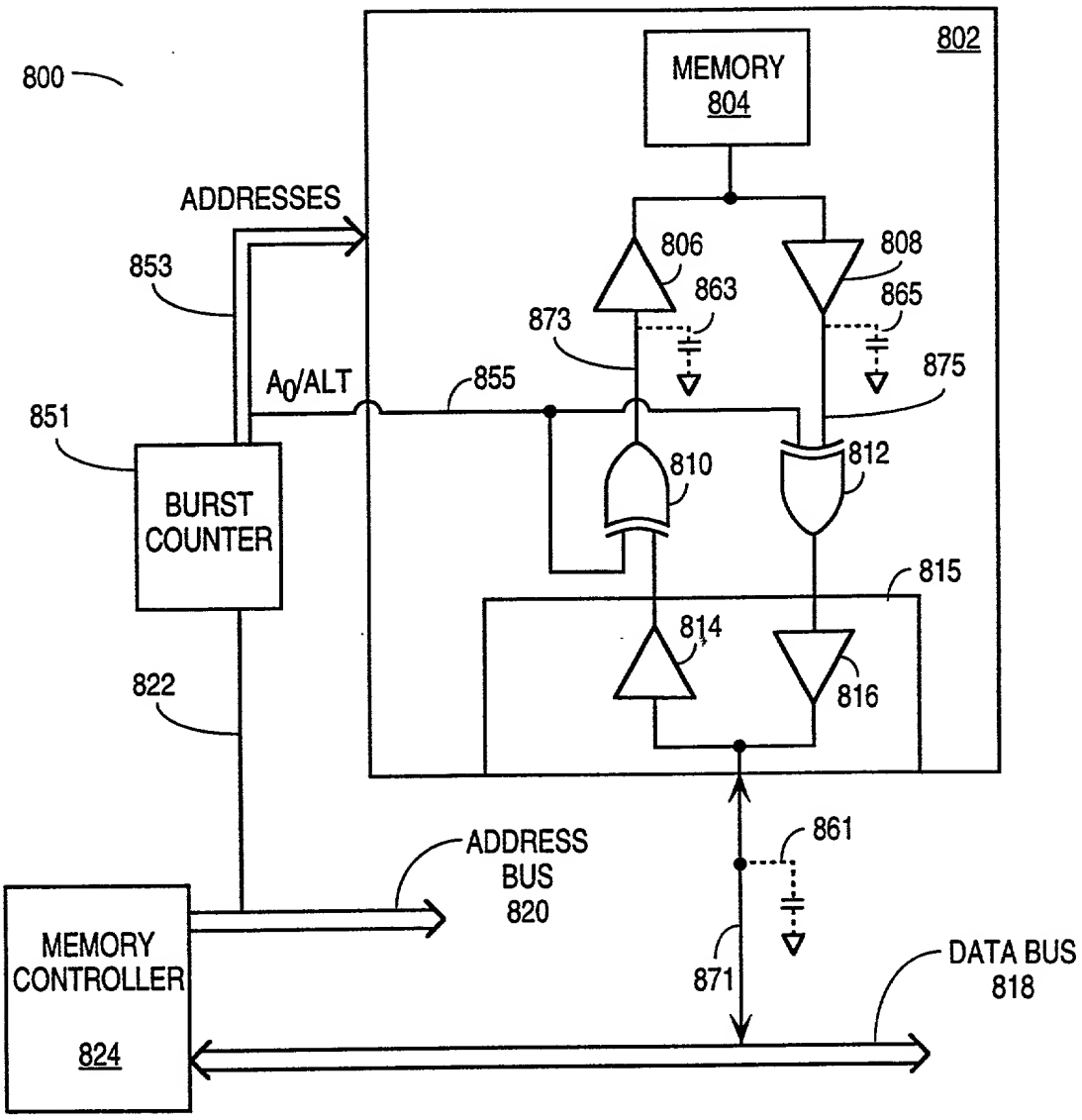

800
802
MEMORY
804
ADDRESSES
853
806
808
863
865
873
875
A0/ALT
855
851
BURST
COUNTER
810
812
815
814
816
822
ADDRESS
BUS
820
861
MEMORY
CONTROLLER
824
871
DATA BUS
818

FIG. 10

TO2080 "958t2660

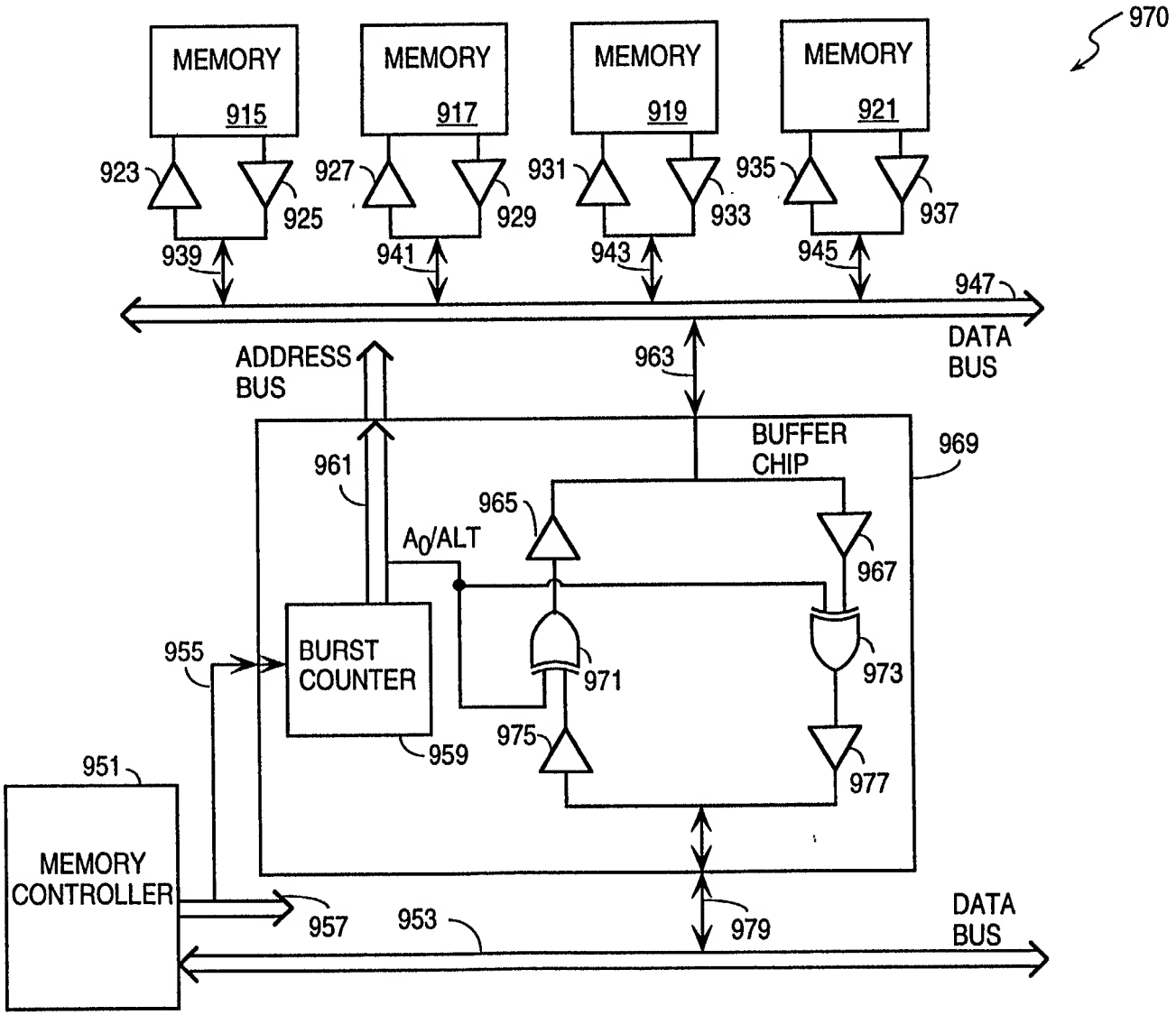


FIG. 11

TO2080 "958h2660

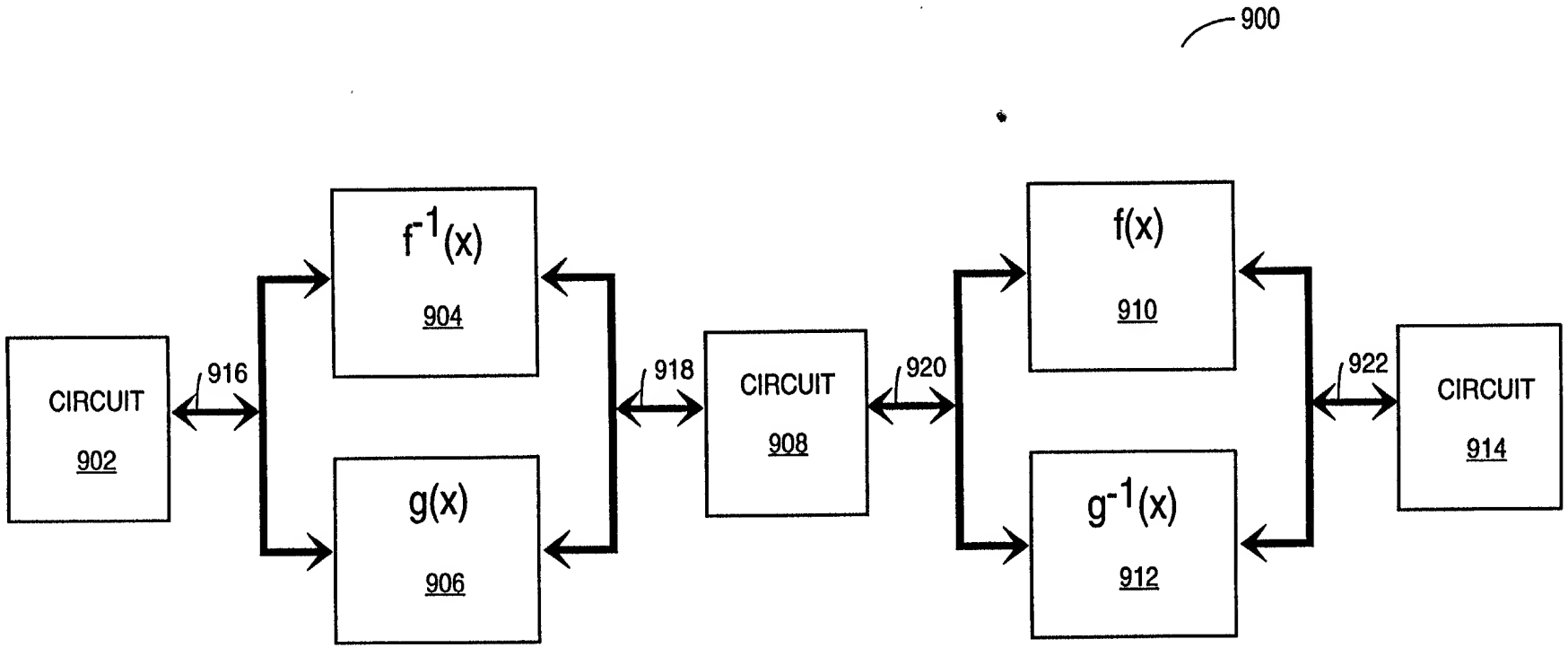


FIG. 12

TO2080 "958n2660

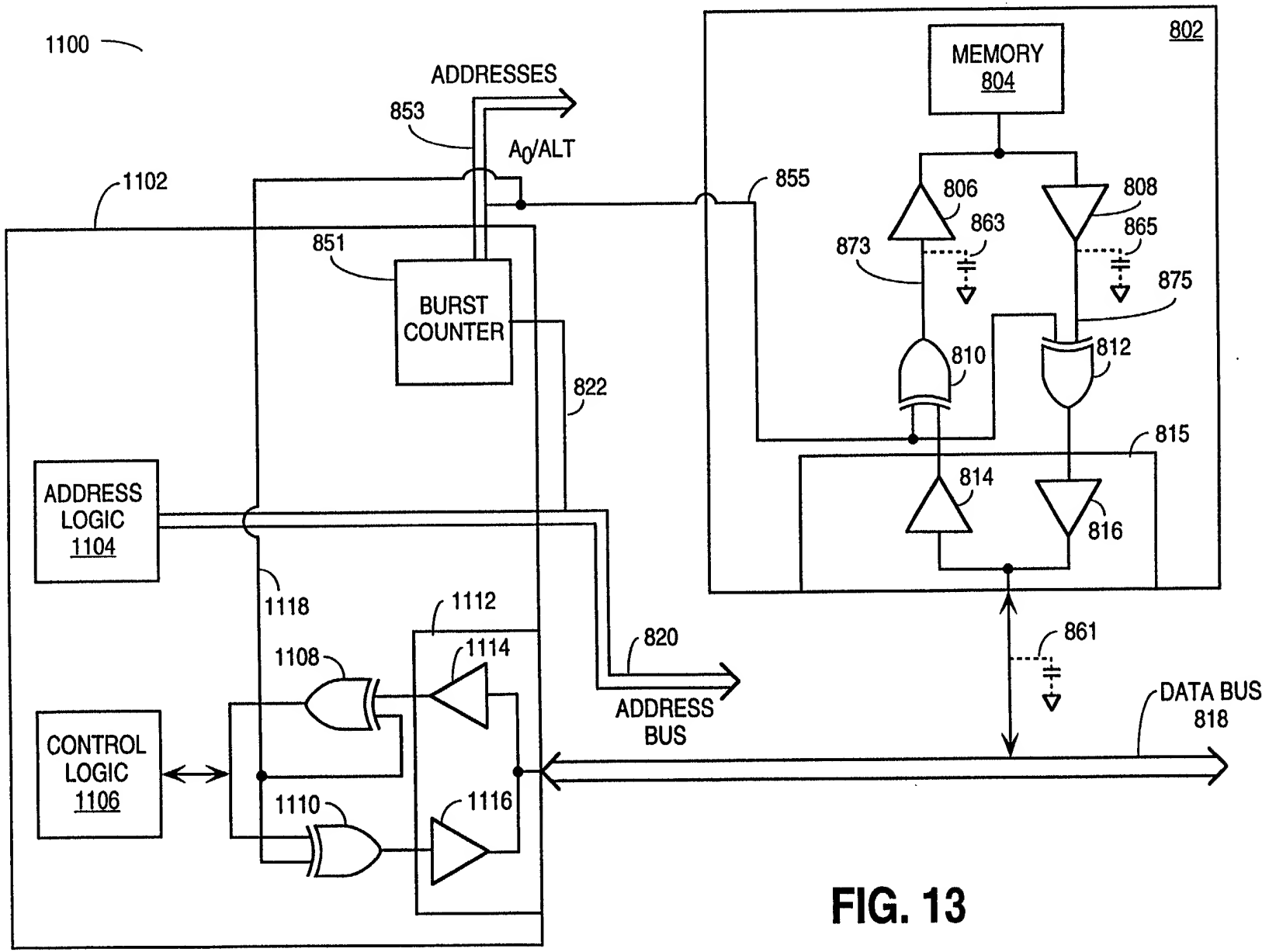


FIG. 13

TO2080 "958m2660

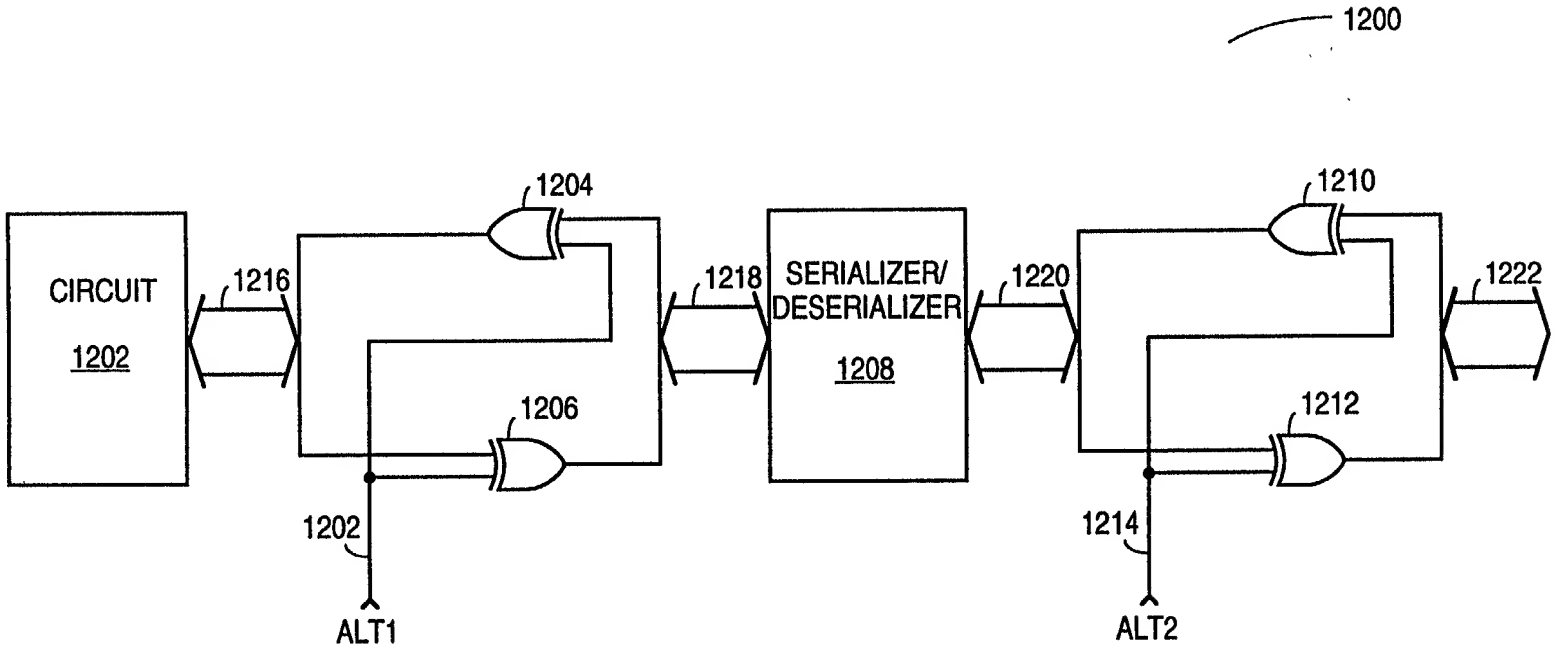


FIG. 14

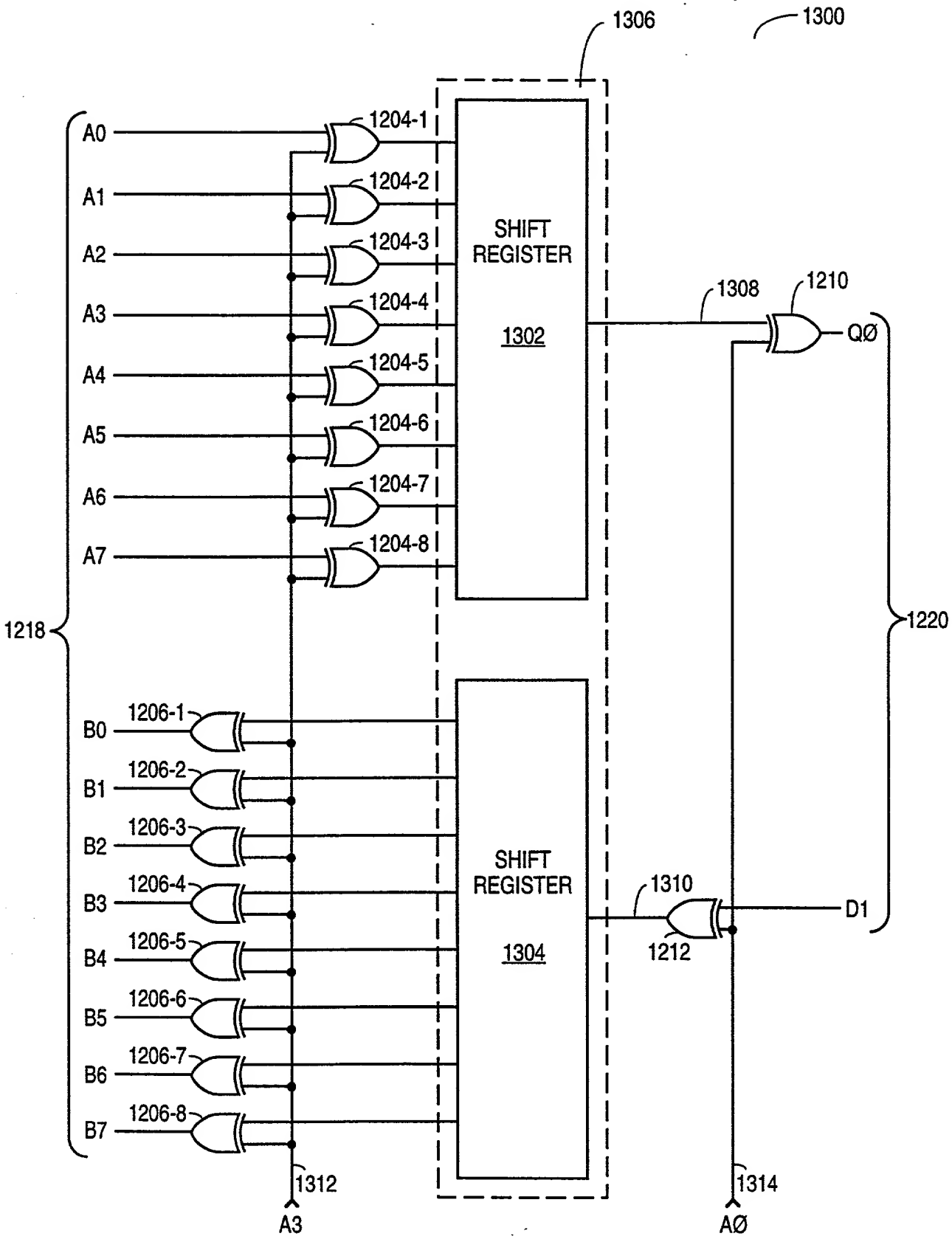

1306
1300
A0
A1
A2
A3
A4
A5
A6
A7
1204-1
1204-2
1204-3
1204-4
1204-5
1204-6
1204-7
1204-8
SHIFT REGISTER
1302
1308
1210
QØ
1218
1220
B0
B1
B2
B3
B4
B5
B6
B7
1206-1
1206-2
1206-3
1206-4
1206-5
1206-6
1206-7
1206-8
SHIFT REGISTER
1304
1310
D1
1212
1312
A3
1314
AØ

FIG. 15

TO2080" 958t2660

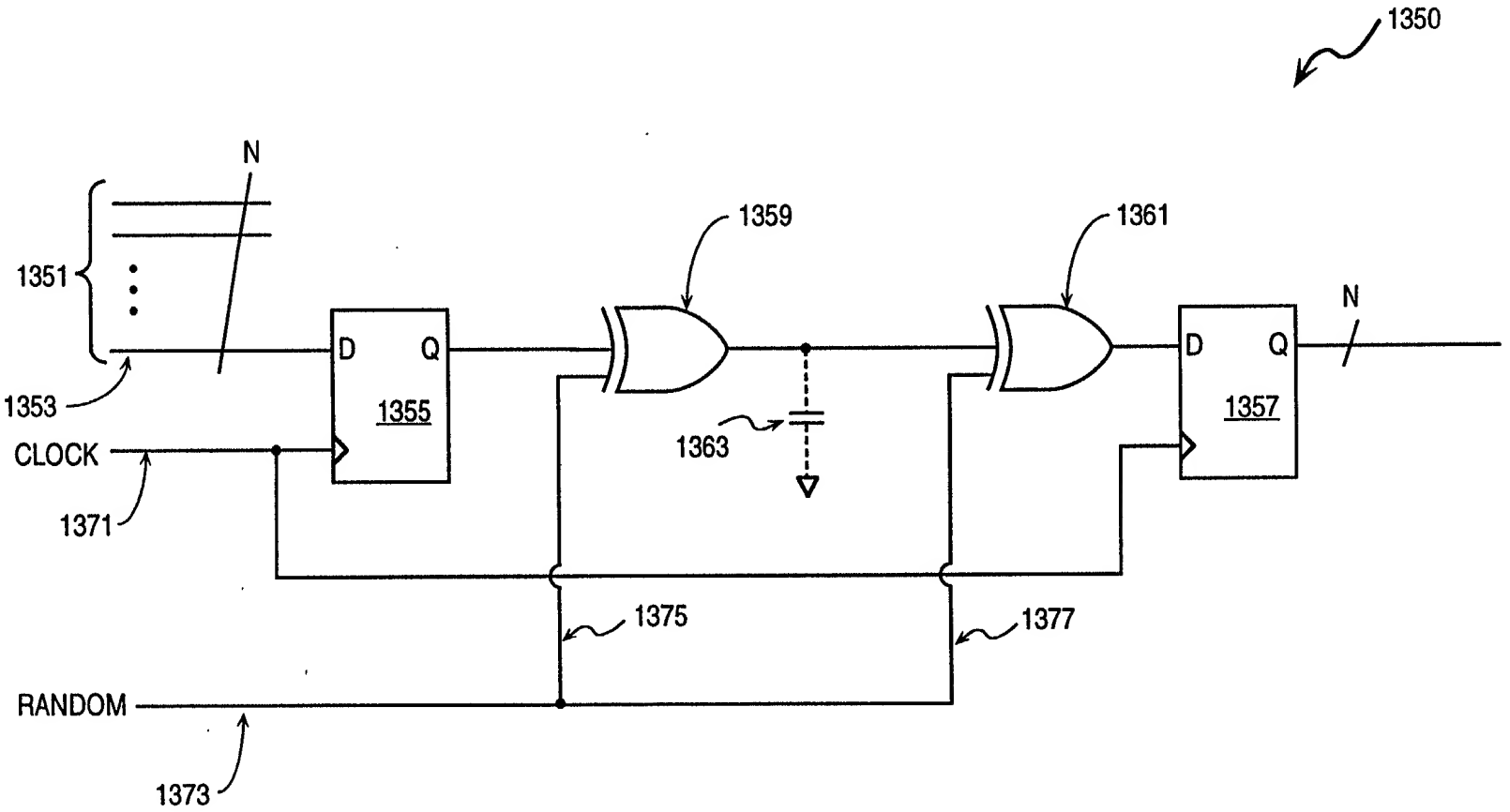


FIG. 16

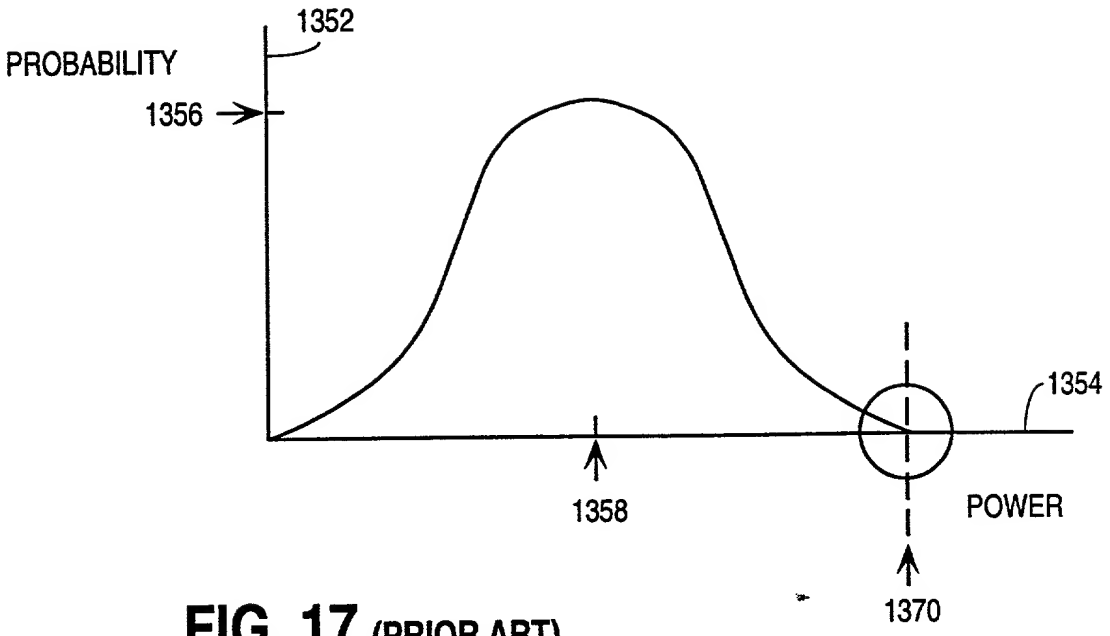


FIG. 17 (PRIOR ART)

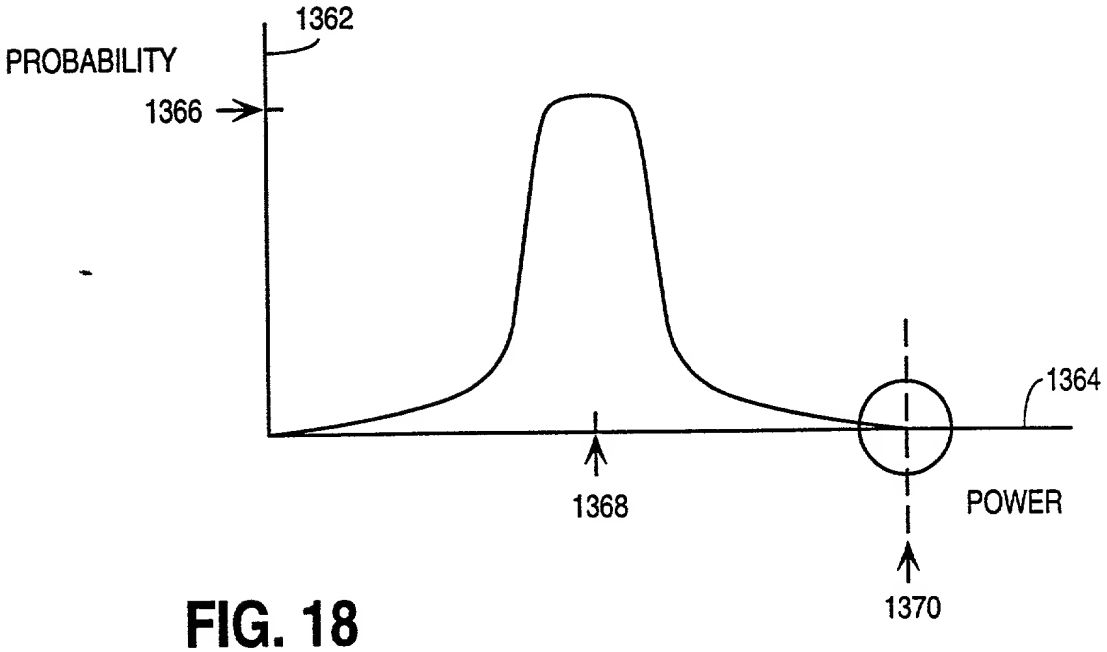


FIG. 18

TD2080 "9581n2660

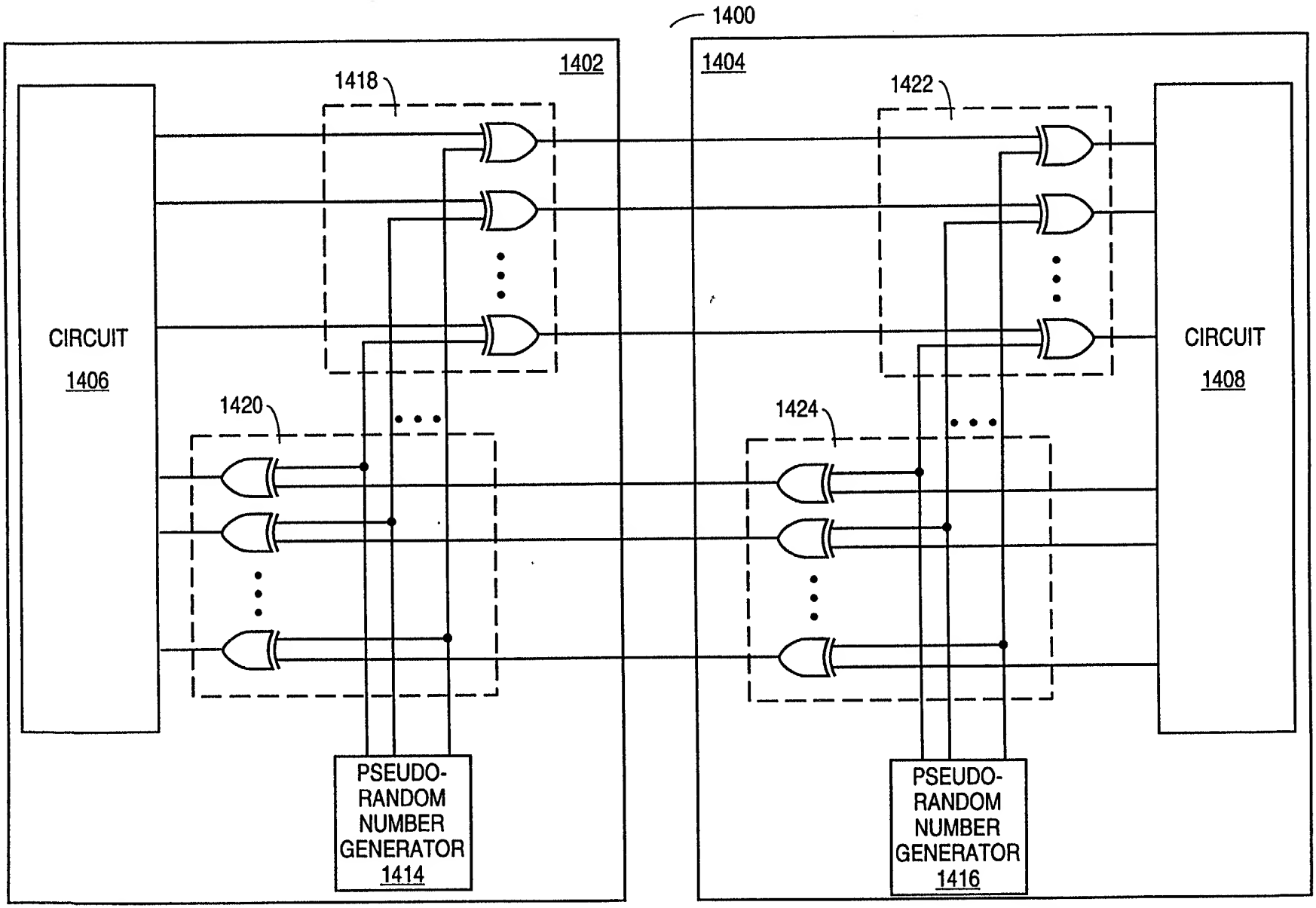


FIG. 19